



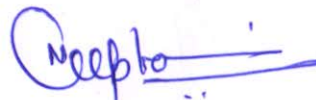
**Shri Vaishnav Vidyapeeth Vishwavidyalaya**  
**Diploma (Electrical Engineering)**  
**Choice Based Credit System (CBCS)**


**SEMESTER-IV (w.e.f. Batch 2019-20)**

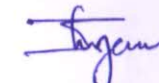
S. No.	COURSE CODE	COURSE NAME	TEACHING SCHEME/WEEK			CREDITS	EXAMINATION SCHEME					TOTAL MARKS
			L	T	P		THEORY			PRACTICAL		
							End Sem University Exam (60%)	Two Term Exam (20%)	Teachers Assessment* (20%)	End Sem University Exam (60%)	Teachers Assessment* (40%)	
1	DTEE401	Power Electronics	3	1	2	5	60	20	20	30	20	150
2	DTEE402	AC Machines	3	1	2	5	60	20	20	30	20	150
3	DTEE403	Utilization of Electrical Energy	3	0	0	3	60	20	20	0	0	100
4	DTEE404	Batteries and Fuel Cells	3	0	0	3	60	20	20	0	0	100
5	DTEE405	Fundamentals of Microprocessor	3	1	2	5	60	20	20	30	20	150
6	DTEE406	Group Discussion	0	0	2	1	0	0	0	0	50	50
			15	3	8	22	300	100	100	90	110	700

Legends: L - Lecture ; T - Tutorial/Teacher Guided Student Activity ; P - Practical ;

\*Teacher Assessment shall be based following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.

  
**Chairperson**  
Board of Studies  
Shri Vaishnav Vidyapeeth Vishwavidyalaya  
Indore

  
**Chairperson**  
Faculty of Studies  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, INDORE

  
**Controller of Examinations**  
Shri Vaishnav Vidyapeeth Vishwavidyalaya

  
**Registrar**  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore

  
**Vice Chancellor**  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore